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- Erik Bruun, DTU (Analog)
- NN, (Digital)



## Call for Papers

# 2016 IEEE NorCAS Conference

IEEE Nordic Circuits and Systems Conference

1-2 November 2016, Copenhagen, Denmark

**Deadline for submission extended to 22 August, 2016**

## General Scope of the Conference

The IEEE NorCAS conference is the main circuits and systems event of the Nordic countries representing both academia as well as electronics industry. The new IEEE NorCAS conference is a merge of the well-established conferences NORCHIP and SoC. The annual IEEE CAS sponsored conference covers all fields of electronics design, spanning from complex digital systems to advanced analog and mixed-mode circuits. The wide scope of the conference promotes cross-field collaboration not only between academics both also with industry. Papers of high scientific and technical quality are presented together with selected invited speakers and pre-conference tutorial sessions.

## Technical programme

The 2016 NorCAS conference will take place on 1-2 November 2016. The Steering Committee invites people from academia and industry to contribute papers. Contributions for the 2016 conference in Copenhagen should be in the following areas:

### ANALOG

- Biomedical Circuits and Systems
- Sensor interface circuits, data-converters
- Wireless Circuits and Systems
- Mixed mode designs, high frequency circuits, design for testability

### DIGITAL

- Microelectronics/nanoelectronics
- Embedded systems, FPGA and rapid prototyping
- Sensors, Imagers, MEMS devices
- Measurement, modeling, and simulation of hardware
- CAE, design tools, VLSI design methodology, neural networks
- Packaging and interconnect
- System-level design flow and methodology

### SoC

- Network-on-Chip
- Hardware/software co-design and reuse techniques
- Multicore and manycore platforms
- Reconfigurable Architectures
- SoC verification and testing
- Processor and memory design for SoC
- Software runtime environments, emulation and simulation

The 2016 NorCAS conference is the successor of the past NORCHIP and SoC conferences.

Papers may be selected for either lecture or poster presentation. Equal quality measures will be applied to posters and lectures. All accepted papers will be published in the conference proceedings and on IEEE Xplore.

Papers will be selected on the basis of a full length paper (4-6 A4 pages) by the Steering Committee based on a peer review made by the Technical Programme Committee. Papers should be submitted no later than 15 August 2016.

## Submission of Contributions

The proceedings will be composed from camera ready pdf files supplied by authors. The paper must follow the IEEE standard as described in the "submission of contributions" section at [www.norcass.org](http://www.norcass.org).

Submitted papers must not exceed six A4 pages with all illustrations and references included. Papers should be submitted as a pdf file no later than 15 August 2016 (see instructions on [www.norcass.org](http://www.norcass.org)).

Authors will be notified in the beginning of October 2016. Papers selected for presentation will be presented in conference proceedings which will be made available upon registration. Proceedings and all presentations will be in English.

## Location

Richmond Hotel  
Vester Farimagsgade 33  
DK-1606 Copenhagen V  
[www.profilhotels.dk](http://www.profilhotels.dk)



[www.norcass.org](http://www.norcass.org)

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